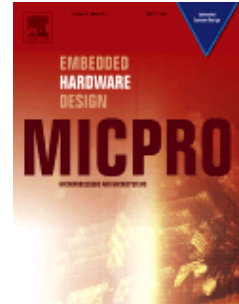


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ISSN: 0141-9331

## DESCRIPTION

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*Microprocessors and Microsystems: Embedded Hardware Design (MICPRO)* is a journal covering all design and architectural aspects related to **embedded systems hardware**. This includes different embedded system hardware **platforms** ranging from custom hardware via reconfigurable systems and application specific processors to general purpose embedded processors. Special emphasis is put on novel **complex embedded architectures**, such as systems on chip (**SoC**), systems on a programmable/reconfigurable chip (SoPC) and multi-processor systems on a chip (**MPSoC**), as well as, their memory and communication methods and structures, such as network-on-chip (**NoC**).

Design automation of such systems including methodologies, techniques, flows and tools for their design, as well as, novel designs of hardware components fall within the scope of this journal. Novel cyber-physical applications that use **embedded systems** are also central in this journal. While software is not in the main focus of this journal, methods of hardware/software co-design, as well as, application restructuring and mapping to embedded hardware platforms, that consider interplay between software and hardware components with emphasis on hardware, are also in the journal scope.

## AUDIENCE

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Microprocessor-systems designers; computer engineers and scientists; applications engineers; original equipment manufacturers; electronics engineers and software developers.

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### INTRODUCTION

*Microprocessors and Microsystems: Embedded Hardware Design* (MICPRO) is a journal covering all design and architectural aspects related to embedded systems hardware. It includes different embedded system hardware platforms ranging from custom hardware via reconfigurable systems and application specific processors to general purpose embedded processors. Special emphasis is put on novel complex embedded architectures, such as systems on chip (SoC), systems on a programmable/reconfigurable chip (SoPC) and multi-processors on a chip (MPoC) as well as their communication methods, such as network-on-chip (NoC).

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#### *Examples:*

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[1] J. van der Geer, J.A.J. Hanraads, R.A. Lupton, The art of writing a scientific article, *J. Sci. Commun.* 163 (2010) 51–59.

Reference to a book:

[2] W. Strunk Jr., E.B. White, *The Elements of Style*, fourth ed., Longman, New York, 2000.

Reference to a chapter in an edited book:

[3] G.R. Mettam, L.B. Adams, How to prepare an electronic version of your article, in: B.S. Jones, R.Z. Smith (Eds.), *Introduction to the Electronic Age*, E-Publishing Inc., New York, 2009, pp. 281–304.

Reference to a website:

[4] Cancer Research UK, Cancer statistics reports for the UK. <http://www.cancerresearchuk.org/aboutcancer/statistics/cancerstatsreport/>, 2003 (accessed 13.03.03).

Reference to a dataset:

[dataset] [5] M. Oguro, S. Imahiro, S. Saito, T. Nakashizuka, Mortality data for Japanese oak wilt disease and surrounding forest compositions, *Mendeley Data*, v1, 2015. <https://doi.org/10.17632/xwj98nb39r.1>.

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