INTEGRATION
the VLSI Journal

AUTHOR INFORMATION PACK

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DESCRIPTION

Integration’s aim is to cover every aspect of the VLSI area, with an emphasis on cross-fertilization between various fields of science, and the design, verification, test and applications of integrated circuits and systems, as well as closely related topics in process and device technologies. Individual issues will feature peer-reviewed tutorials and articles as well as reviews of recent publications. The intended coverage of the journal can be assessed by examining the following (non-exclusive) list of topics:

- Specification methods and languages;
- Analog/Digital Integrated Circuits and Systems;
- VLSI architectures;
- Algorithms, methods and tools for modeling, simulation, synthesis and verification of integrated circuits and systems of any complexity;
- Embedded systems;
- High-level synthesis for VLSI systems;
- Logic synthesis and finite automata;
- Testing, design-for-test and test generation algorithms;
- Physical design;
- Formal verification;
- Algorithms implemented in VLSI systems;
- Systems engineering;
- Heterogeneous systems.

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T. Kim, Seoul National University (SNU), Seoul, The Republic of Korea
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S. Nakatake, University of Kitakyushu, Kitakyushu, Japan
• Physical design, Combinatorial algorithm, Analog layout, Analog circuit design, A/D converter, Sensor system
N. Nedjah, Universidade do Estado do Rio de Janeiro, Maracanã, Rio de Janeiro, Brazil

• Network-on-chip, Reconfigurable hardware, Bio-inspired architectures
V.F. Pavlidis, University of Manchester, Manchester, England, UK

• On-chip interconnects, 3-D ICs, Clock distribution networks, Power distribution networks
G. Qu, University of Maryland, College Park, Maryland, USA

• Hardware security, Trusted IC, Intellectual property protection, Low power
E. Ragonese, Università di Catania, Catania, Italy

RF integrated circuits (ICs), mm-wave ICs, isolated dc-dc converters, radar sensors
S. Reda, Brown University, Providence, Rhode Island, USA

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• Arithmetic circuits, Approximate computing, Flip-flops
M. Tahoori, Karlsruhe Institute of Technology (KIT), Germany

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• Ultra low power CMOS, Analog IC, Wireless transceivers, Serial links, Wide band PLL, Data converters
Y. Takashima, University of Kitakyushu, Kitakyushu, Japan

• Physical layout algorithm, Floorplan, Placement, routing algorithm, Scheduling and timing
E. Tlelo-Cuautle, Instituto Nacional de Astrofísica, Óptica y Electrónica, Puebla, Mexico

• Algorithms, Methods and tools for modeling, Simulation, Synthesis and verification of integrated circuits and systems of any complexity, Circuit optimization
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Y. Wang, Syracuse University, Syracuse, New York, USA

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R. Wille, University of Linz, Linz, Austria

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D. Wong, University of California, Riverside, Riverside, California, USA

• Energy Efficiency, Approximate Computing, Computer Architecture, Reconfigurable Computing, GPUs
Q. Xu, The Chinese University of Hong Kong, Hong Kong, China

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J. Yin, University of Macau, Taipa, Macau, China

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S. Yin, Tsinghua University, Beijing, China

• Reconfigurable computing, high level synthesis, Neural network processor, Energy efficient VLSI design
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• Low power, Power and signal integrity, 3D IC, Cross-layer design and optimization, Design for reliability
  **C. Zhuo**, Zhejiang University, Hangzhou, China
GUIDE FOR AUTHORS

Types of contributions
Integration's aim is to cover every aspect of the VLSI area, with an emphasis on cross-fertilization between various fields of science, and the design, verification, test and applications of integrated circuits and systems, as well as closely related topics in process and device technologies. Individual issues will feature peer-reviewed tutorials and articles as well as reviews of recent publications. The intended coverage of the journal can be assessed by examining the following (non-exclusive) list of topics:

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Acknowledgements
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